

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. KATAYAMA, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application
Serial No. 10/304,046, filed November 26, 2002

Filed: On even date herewith

For: A SEMICONDUCTOR MEMORY HAVING
ELECTRICALLY ERASABLE AND PROGRAMMABLE
SEMICONDUCTOR MEMORY CELLS (As Amended)

Art Group of Parent: 2818

Examiner of Parent: T. Q. Le

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 16, 2003

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Title and Specification and

Remarks follow the amendments